

LOGICAL CIRCUIT DESIGN APPARATUS 50

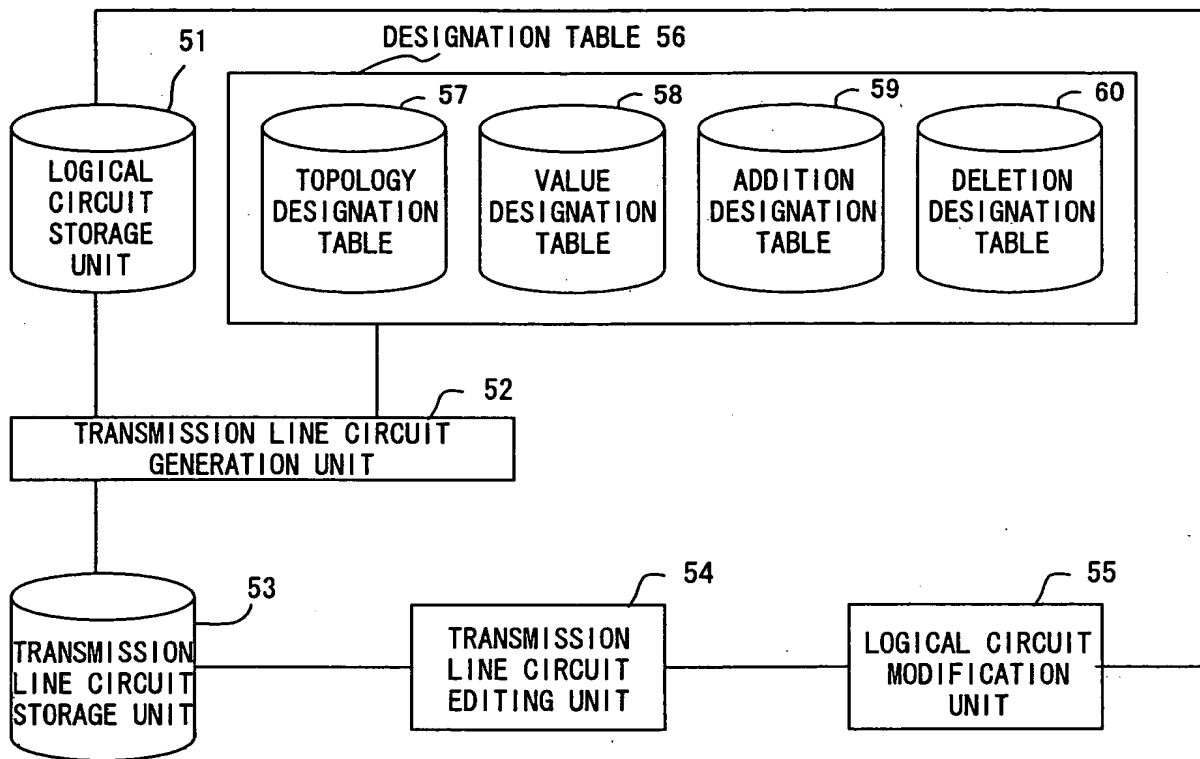


FIG. 1

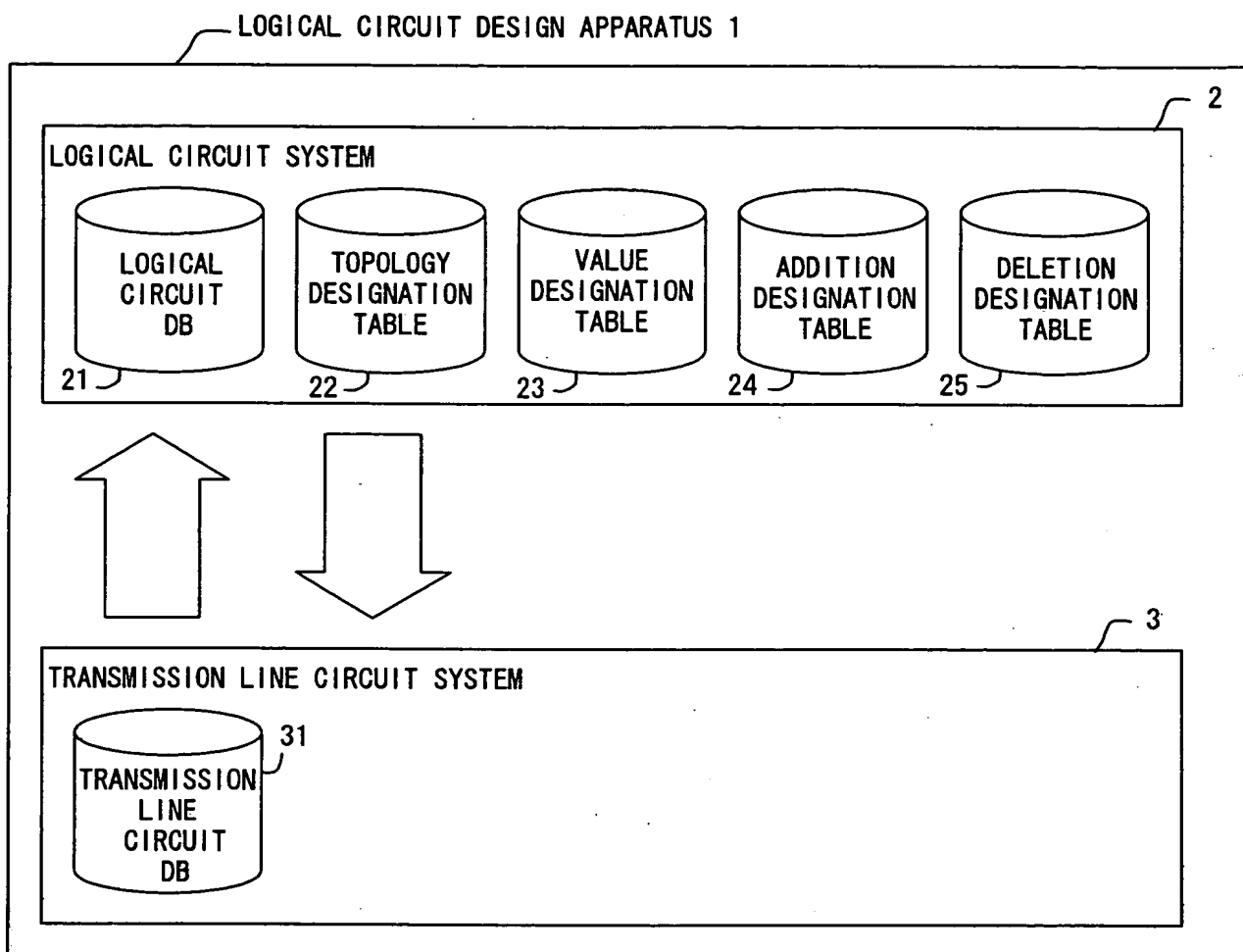


FIG. 2

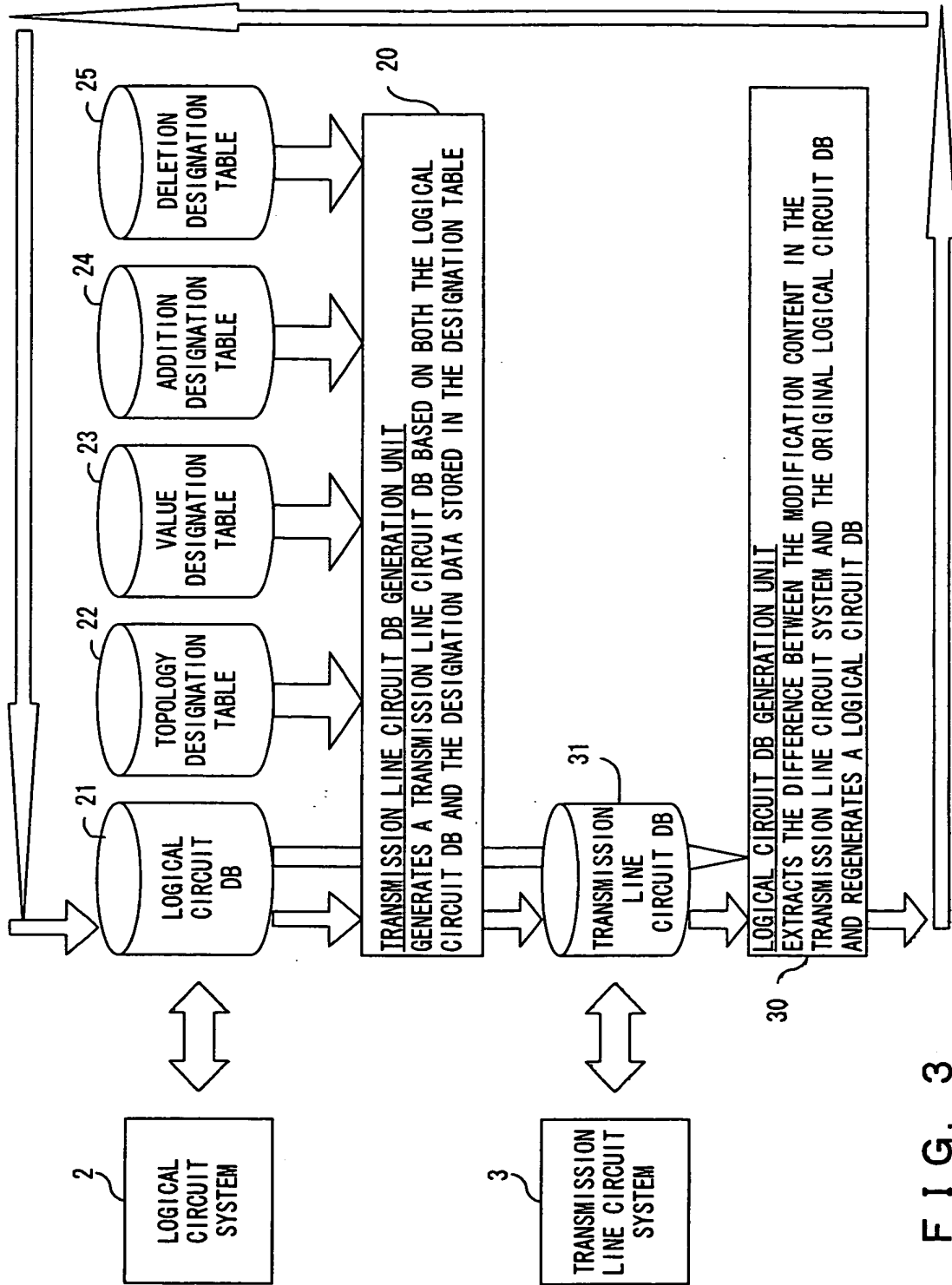
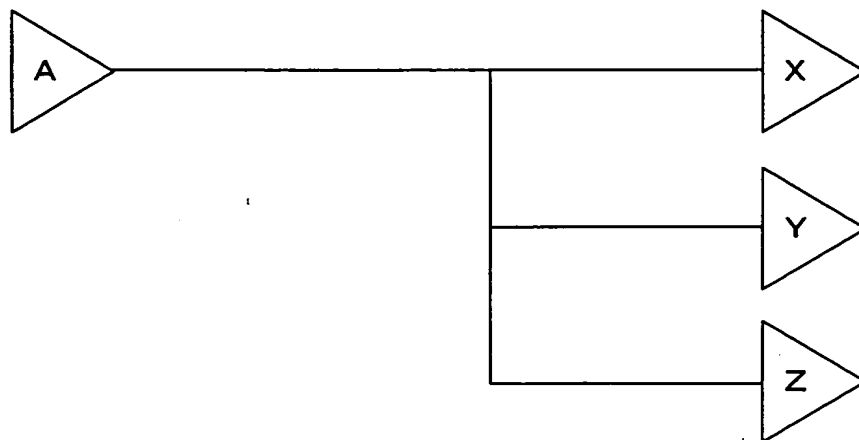


FIG. 3



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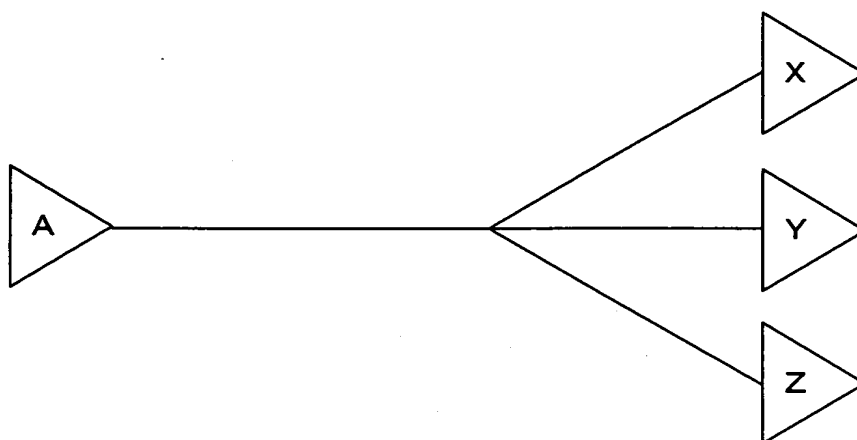


FIG. 5

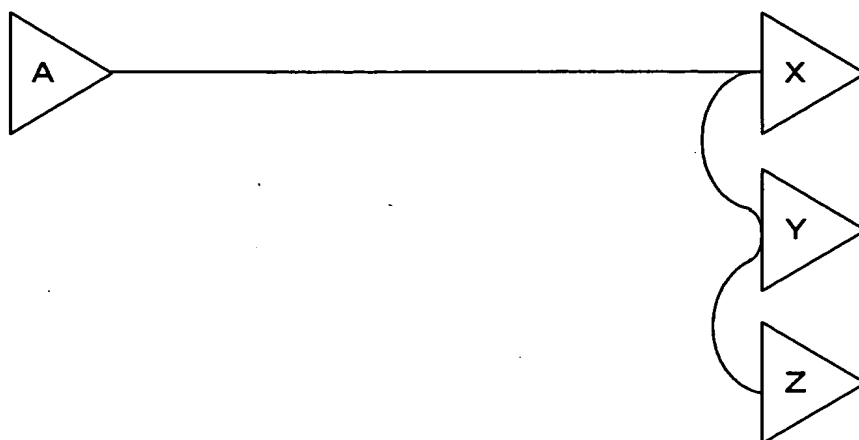


FIG. 6

TOP SECRET

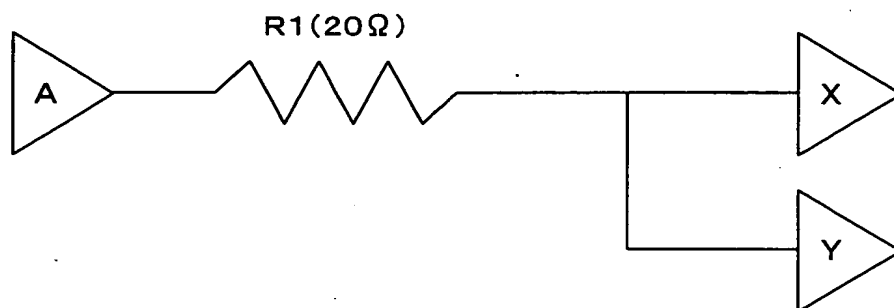


FIG. 7

ITEM NO.	SYMBOL	COMPONENT DESCRIPTION	VALUE
1	R1	RESISTOR	33 Ω

FIG. 8

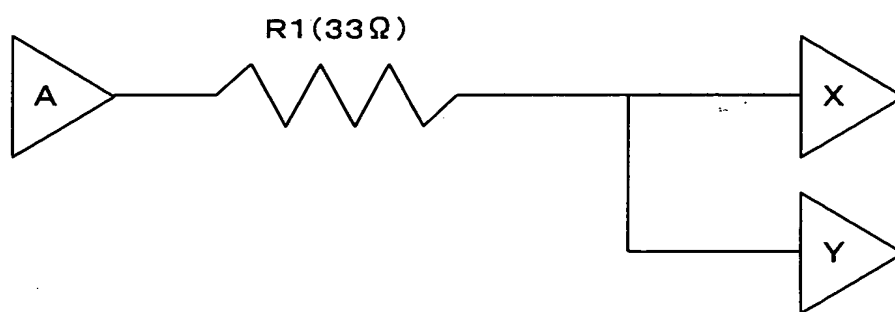


FIG. 9

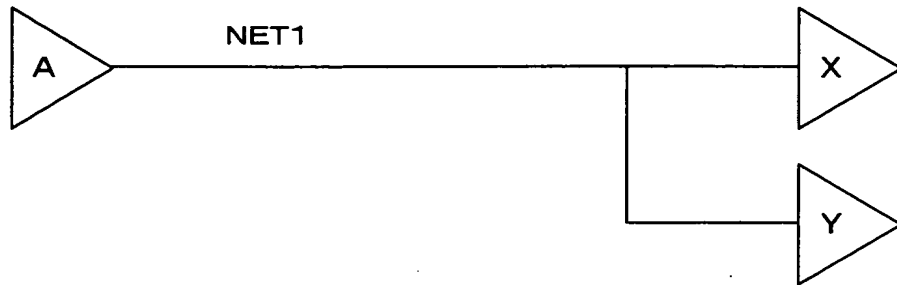


FIG. 10

FIG. 11

ITEM NO.	NET	LOCATION	COMPONENT DESCRIPTION	VALUE
1	NET1	BY THE SIDE OF A	RESISTOR	33 Ω

FIG. 11

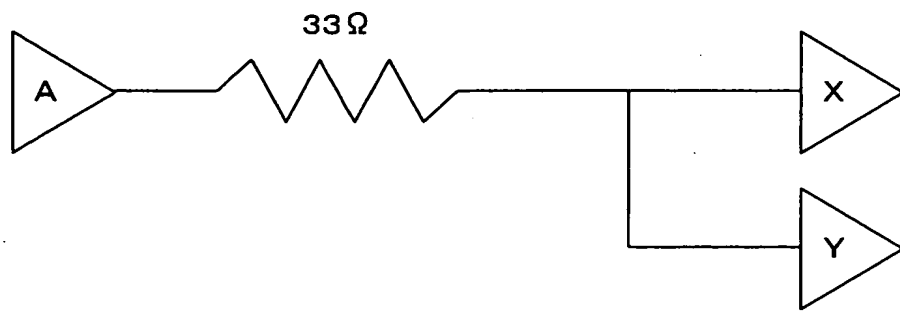


FIG. 12

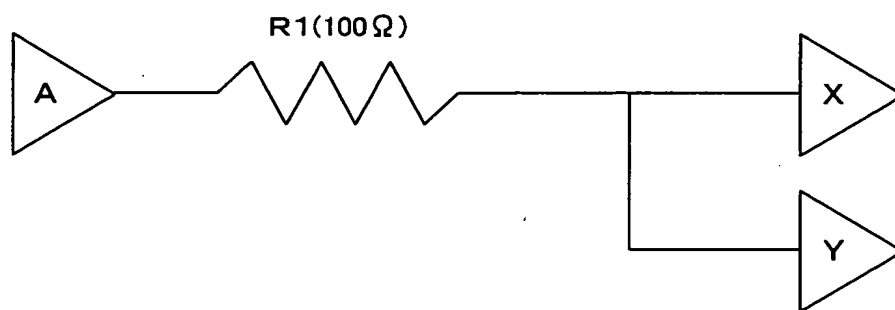


FIG. 13

ITEM NO.	SYMBOL
1	R1

FIG. 14

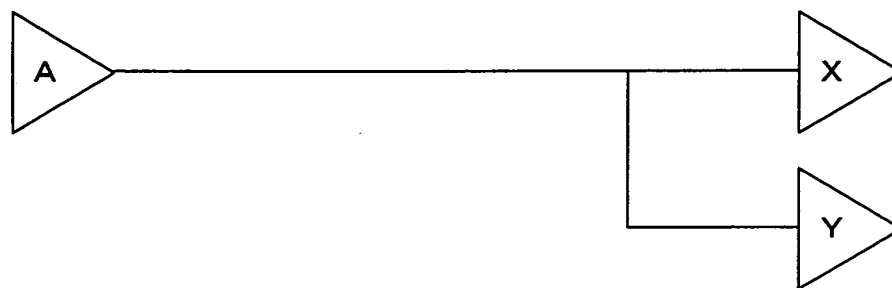


FIG. 15

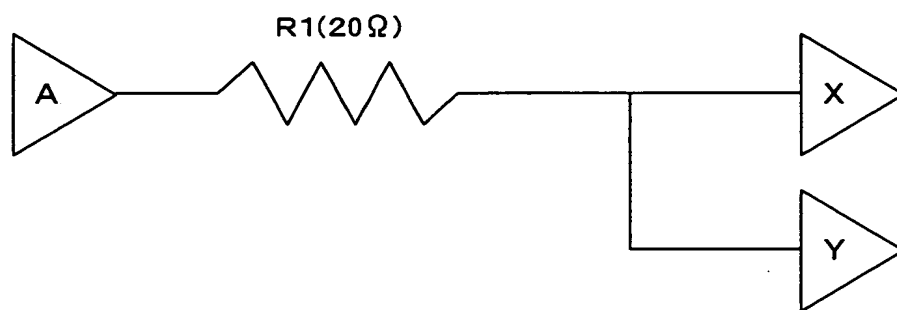


FIG. 16

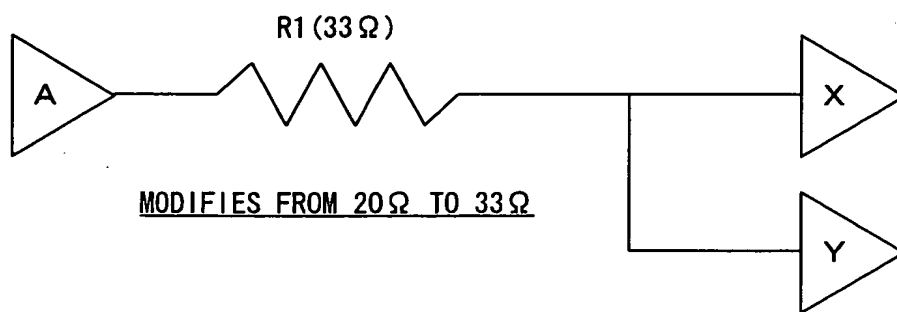


FIG. 17

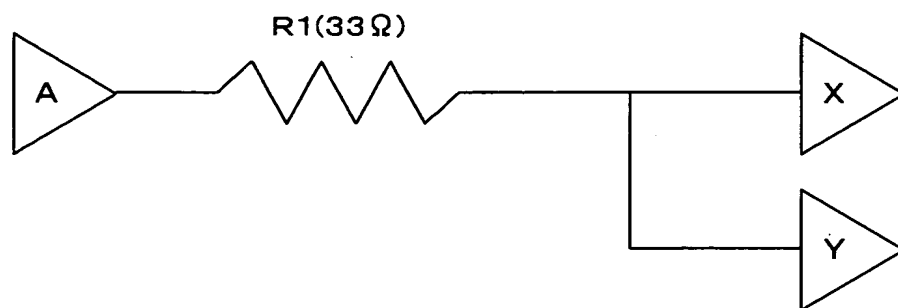


FIG. 18

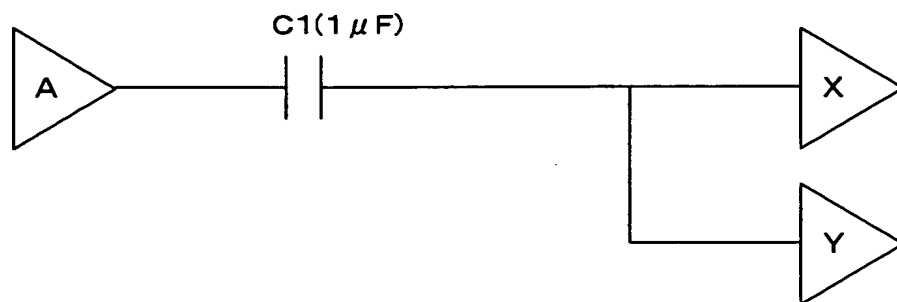


FIG. 19

[Faint bleed-through from the reverse side of the page]

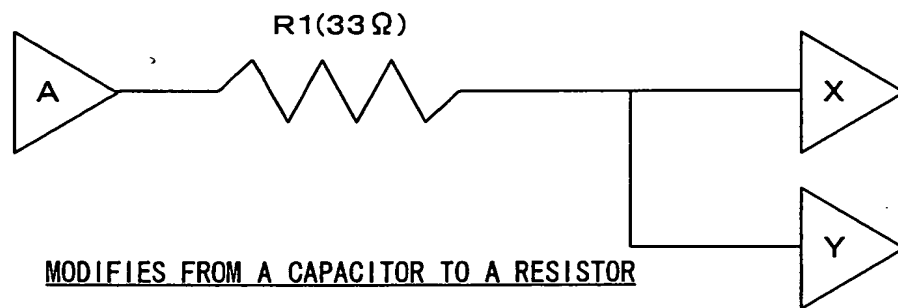


FIG. 20

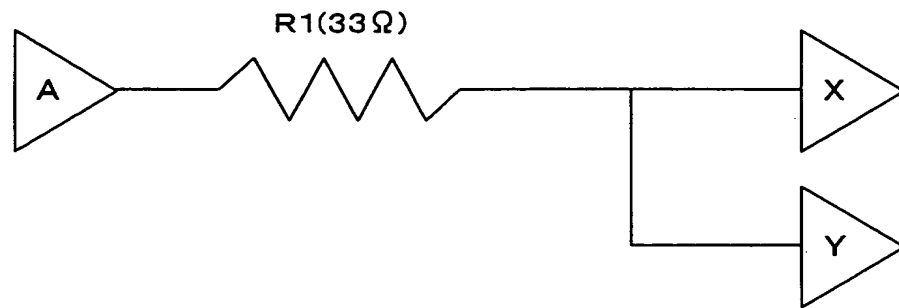


FIG. 21

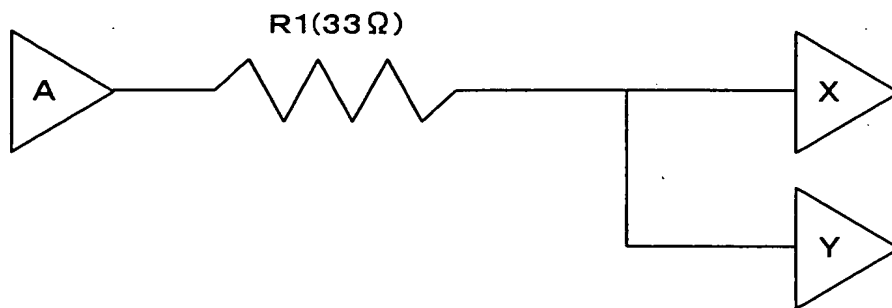


FIG. 22

404260-1050900

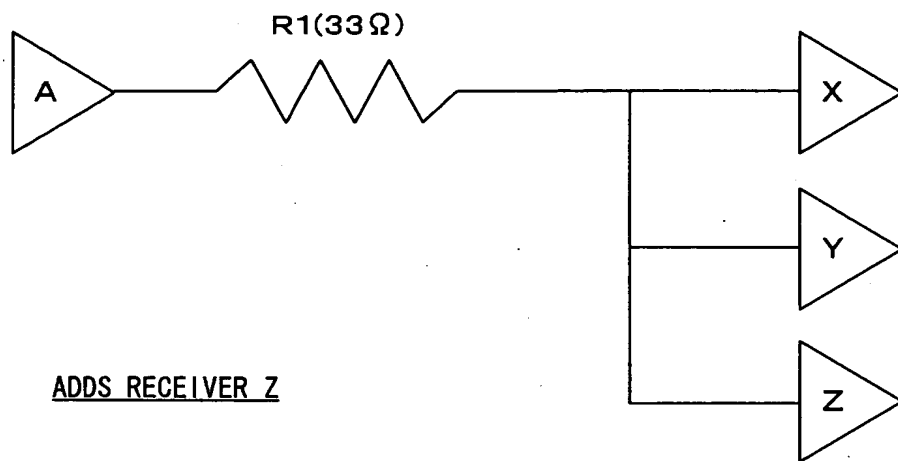


FIG. 23

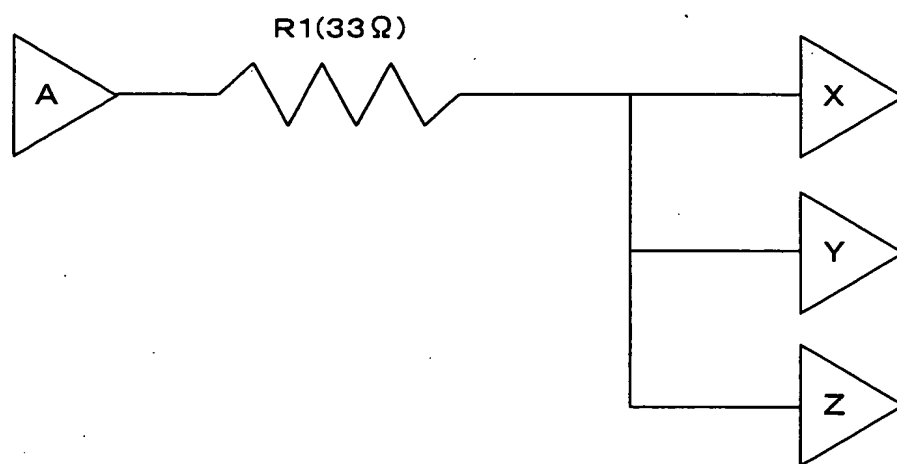


FIG. 24

FIG. 25

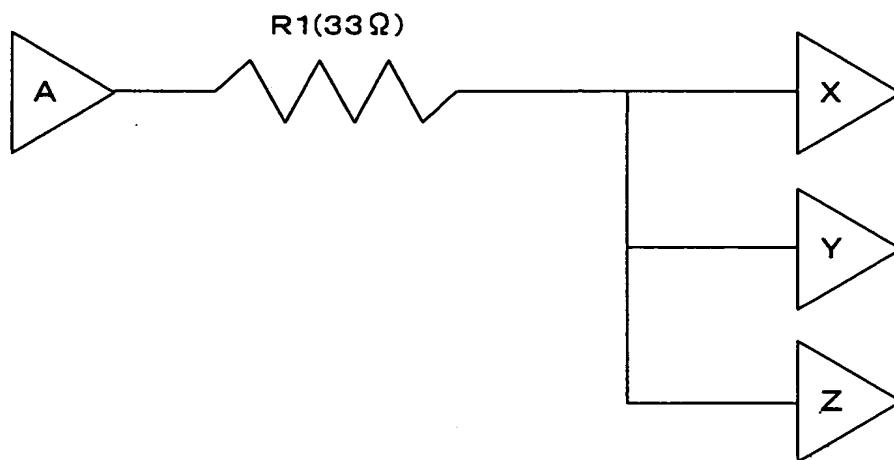


FIG. 25



FIG. 26

104260-1030560

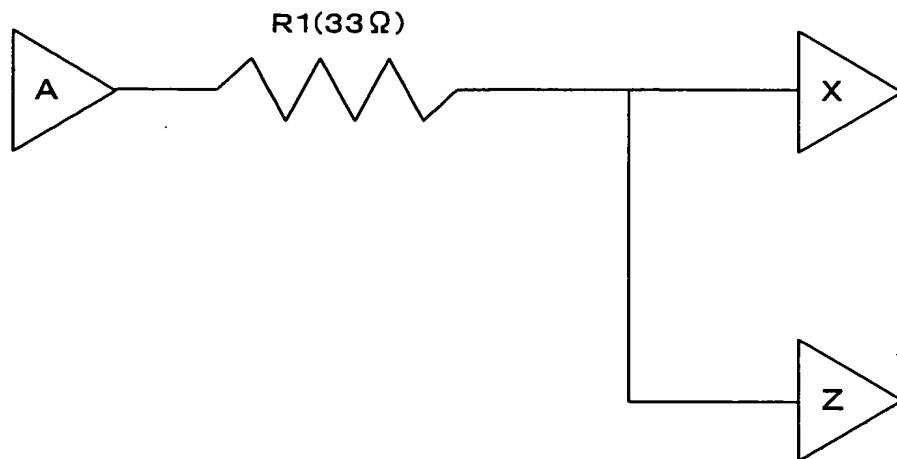


FIG. 27

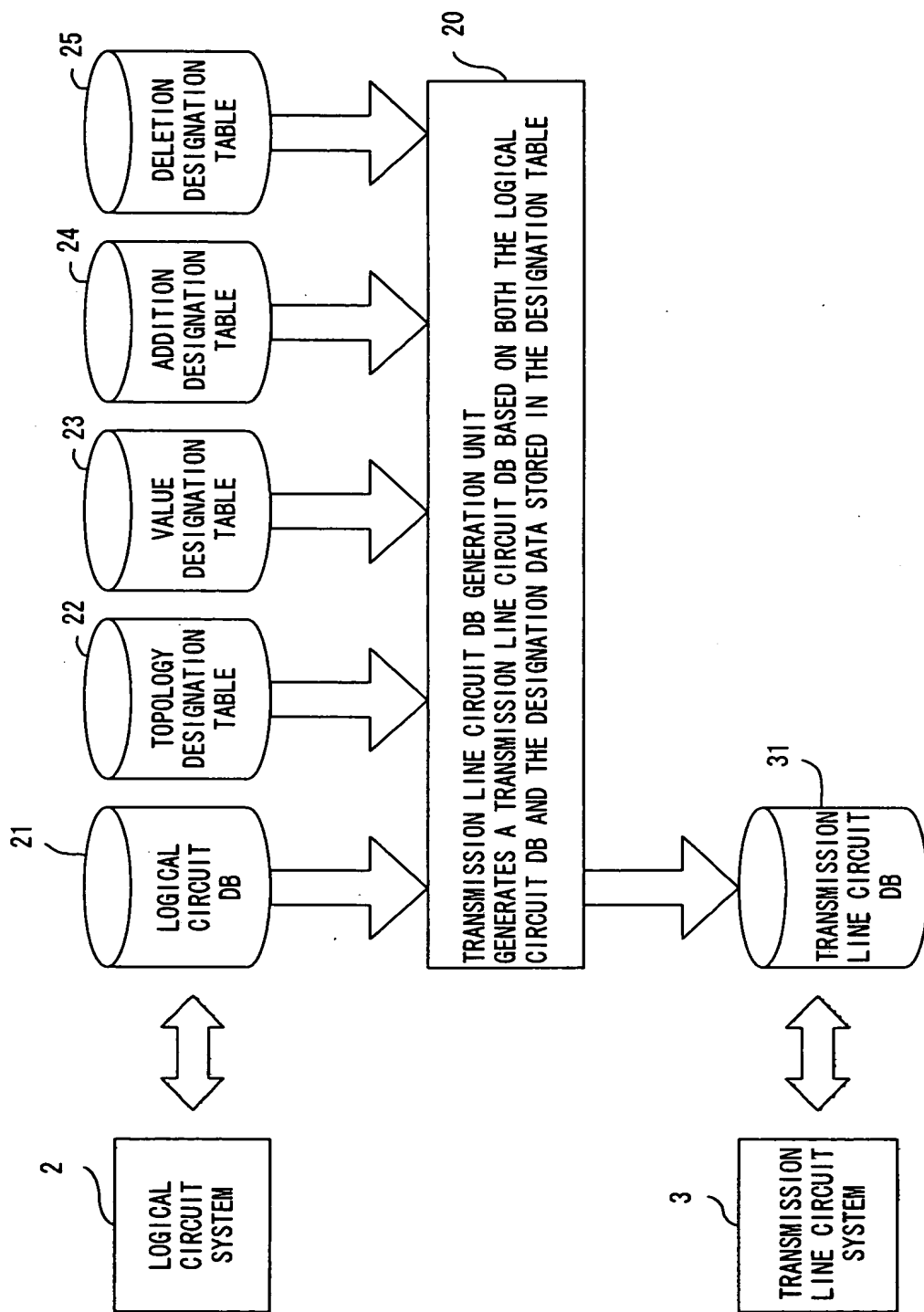


FIG. 28

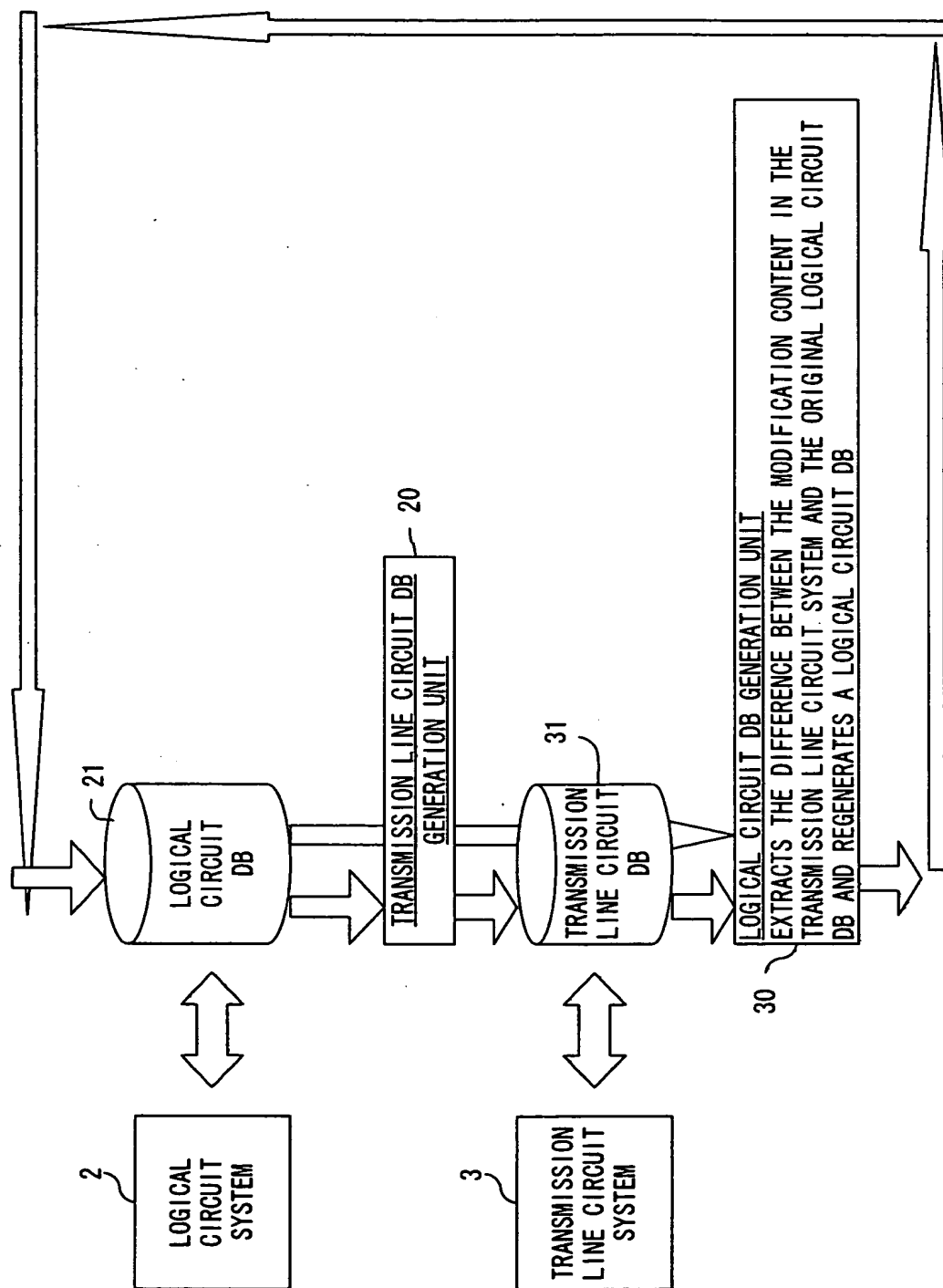


FIG. 29

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graph LR
    CPU[141 CPU] --- BUS
    MEM[142 MEMORY] --- BUS
    IN[143 INPUT DEVICE] --- BUS
    OUT[144 OUTPUT DEVICE] --- BUS
    BUS --- EX[145 EXTERNAL STORAGE DEVICE]
    BUS --- MD[147 MEDIUM DRIVING DEVICE]
    BUS --- NC[148 NETWORK CONNECTION DEVICE]
    MD --> PSM[146 PORTABLE STORAGE MEDIUM]
    NC <--> NET[NETWORK]
  
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FIG. 30

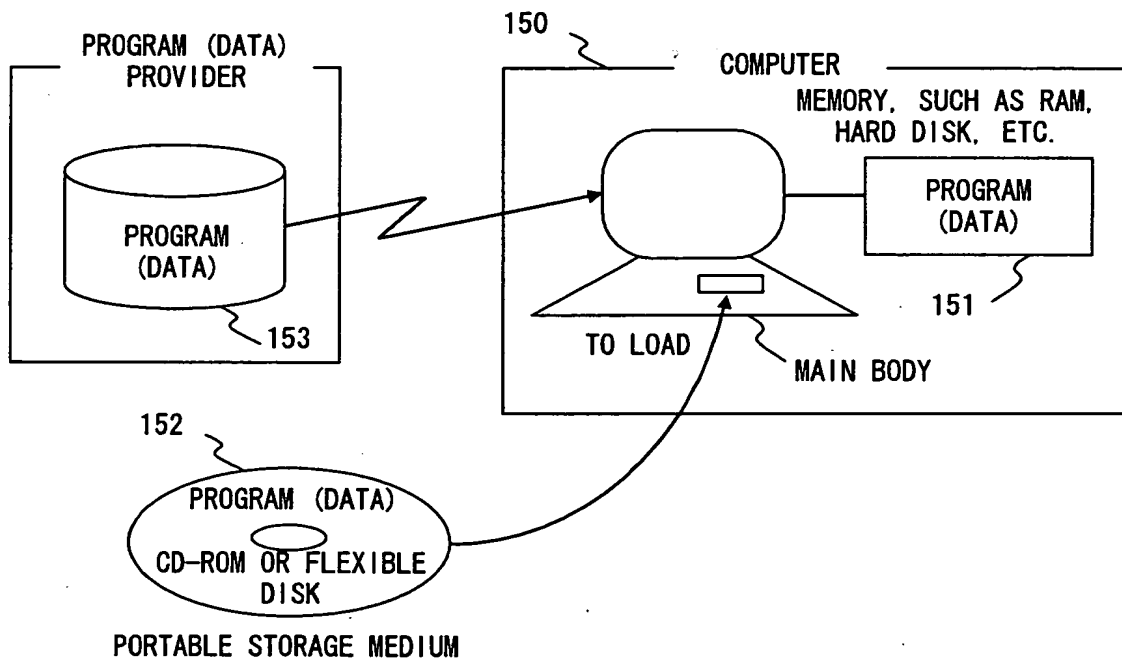


FIG. 31